

MODEL NO : LCDT351923701-L

MODEL VERSION: 01

SPEC VERSION : 2.9

ISSUED DATE: 2015-12-08

- ☐ Preliminary Specification
- ☒ Final Product Specification

Customer : _____

App oved by	Notes

LCD Confirmed :

Prepared by	Checked by	Approved by
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This technical specification is subjected to change without notice

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[illegible]

1 General Specifications

Feature		Spec
Display Spec.	Size	3.5"
	Resolution	320(RGB)x480
	Technology Type	a-si TFT
	Pixel Configuration	RGB Vertical Stripe
	Pixel pitch(mm)	0.180 x 0.180
	Display Mode	HVT,NW
	Surface Treatment	Clear Type
	Viewing Direction	ALL o'clock
	Gray Scale Inversion Direction	ALL o'clock
Mechanical Characteristics	LCM (W x H x D) (mm)	54.66x82.96x2.3
	Active Area(mm)	48.96x73.44
	With /Without TSP	With RTP
	Matching Connection Type	37pin-1.0mm
	LED Numbers	6
	Weight (g)	18
Electrical Characteristics	Interface	CPU 8/16 bits
	Color Depth	262K
	Driver IC	ILI9488

Note 1: Viewing direction for best image quality is different from TFT definition. There is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance: $\pm 5\%$

2 Input/Output Terminals

Matched connector: 37pin-1.0mm

Pin No.	Symbol	I/O	Function	Remark
1	GND	P	Power Ground	
2	VDD	P	Power Supply for Analog	
3	VDD	P	Power Supply for Analog	
4	CS	I	Chip select signal	
5	RS	I	Register select signal	
6	WR	I	Write enables signal	
7	RD	I	Read enables signal	
8	RESET	I	Reset signal	
9	DB0	IO	Data Input	
10	DB1	IO	Data Input	
11	DB2	IO	Data Input	
12	DB3	IO	Data Input	
13	DB4	IO	Data Input	
14	DB5	IO	Data Input	
15	DB6	IO	Data Input	
16	DB7	IO	Data Input	
17	DB8	IO	Data Input	
18	DB9	IO	Data Input	
19	DB10	IO	Data Input	
20	DB11	IO	Data Input	
21	DB12	IO	Data Input	
22	DB13	IO	Data Input	
23	DB14	IO	Data Input	
24	DB15	IO	Data Input	
25	GND	P	Power Ground	
26	Y-	P	TOUCH YD	
27	X-	P	TOUCH XR	
28	Y+	P	TOUCH YU	
29	X+	P	TOUCH XL	
30	LEDK1	P	LED light cathode	
31	LEDK2	P	LED light cathode	
32	LEDK3	P	LED light cathode	
33	LEDK4	P	LED light cathode	
34	LEDK5	P	LED light cathode	
35	LEDA	P	LED light anode	
36	LEDA	P	LED light anode	
37	GND	P	Power Ground	

Note1, Please add the FPC connector type and matched one if necessary .

3 Absolute Maximum Ratings

GND=0V					
Item	Symbol	MIN	MAX	Unit	Remark
Power Voltage	VCC	-0.3	4.6	V	Note1
Input voltage	V _{IN}	-0.3	4.6	V	
Operating Temperature	Top	-20	70	°C	
Storage Temperature	Tst	-30	80	°C	
Relative Humidity Note2	RH	--	"95	%	Ta"40°C
		--	"85	%	40°C<Ta"50°C
		--	"55	%	50°C<Ta"60°C
		--	"36	%	60°C<Ta"70°C
		--	"24	%	70°C<Ta"80°C
Absolute Humidity	AH	--	"70	g/m*	Ta>70°C

Table 3 Absolute Maximum Ratings

Note1: Input voltage include R0~R5, G0~G5, B0~B5, Dotclk, Hsync, Vsync, Enable, R/L, U/D.

Note2: Ta means the ambient temperature.

It is necessary to limit the relative humidity to the specified temperature range.

Condensation on the module is not allowed.

4 Electrical Characteristics

4.1 LCD Module

GND=0V, Ta=25?

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Logic Supply Voltage		IOVCC	2.5	2.8	3.3+/-10%	V	
Analog Supply Voltage		VCC	2.5	2.8	3.3+/-10%	V	
Input Signal Voltage	High Level	VIH	0.7 IOVCC	-	IOVCC	V	
	Low Level	VIL	-	-	0.3 IOVCC	V	
Output Signal Voltage	High Level	VOH	0.8 IOVCC	-	-	V	
	Low Level	VOL	-	-	0.2 IOVCC	V	
(Panel+LSI) Power Consumption	Black Mode		-	30	36	mW	
	Sleeping Mode		-	0.047	0.057	mW	

Table 4.1 LCD module electrical characteristics

4.2 Backlight Unit

Ta=25?

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current		IF	-	15	-	mA	One LED
Forward Voltage		VF	(2.9)	3.2	(3.4)	V	One LED
Backlight Power Consumption		WBL	-	192	-	mW	6 LEDs
Lifetime		T	-	20000	-	Hr	One LED

Table 4.2.1 backlight unit electrical characteristics

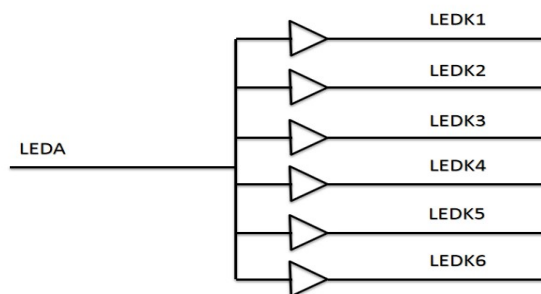
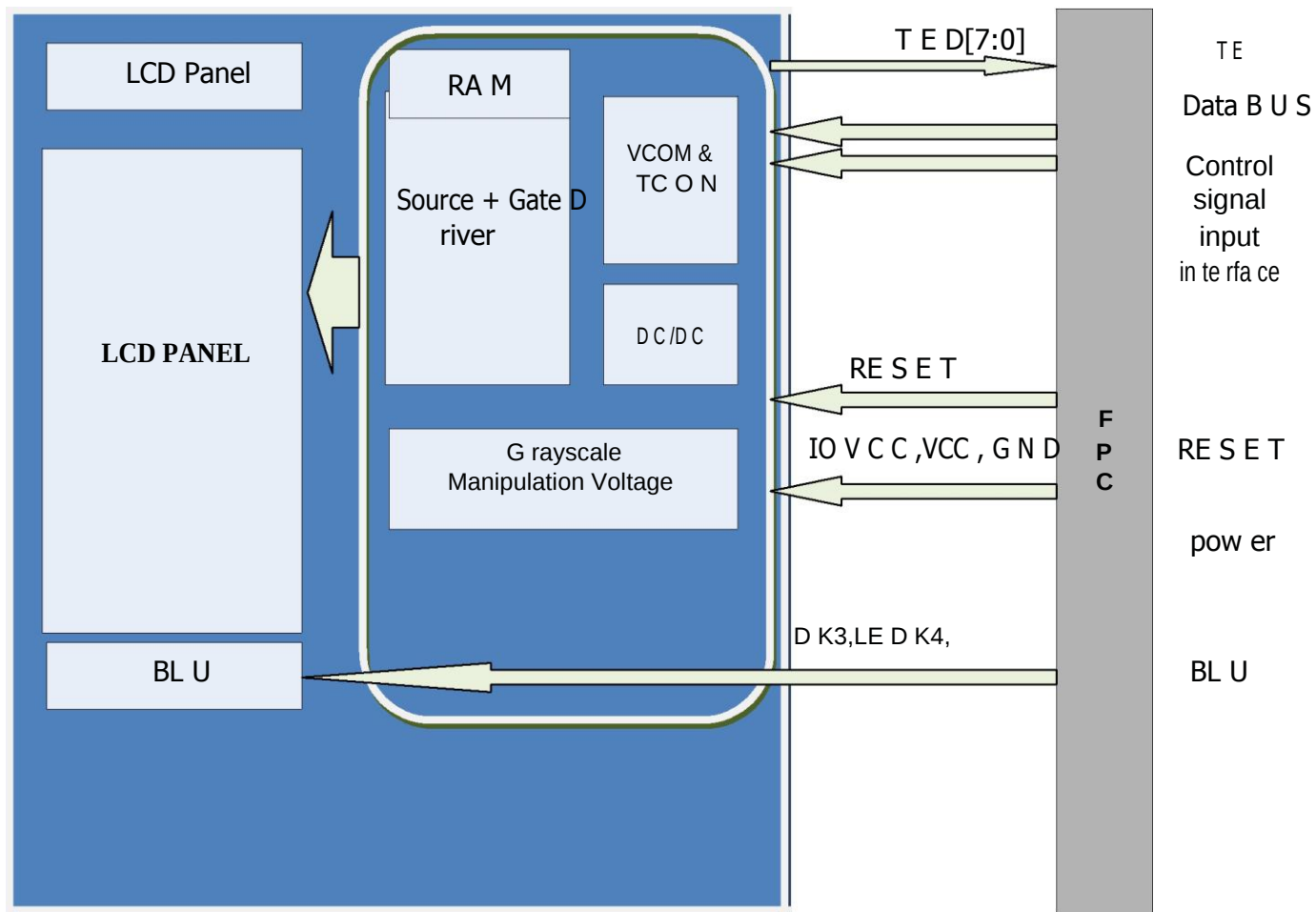
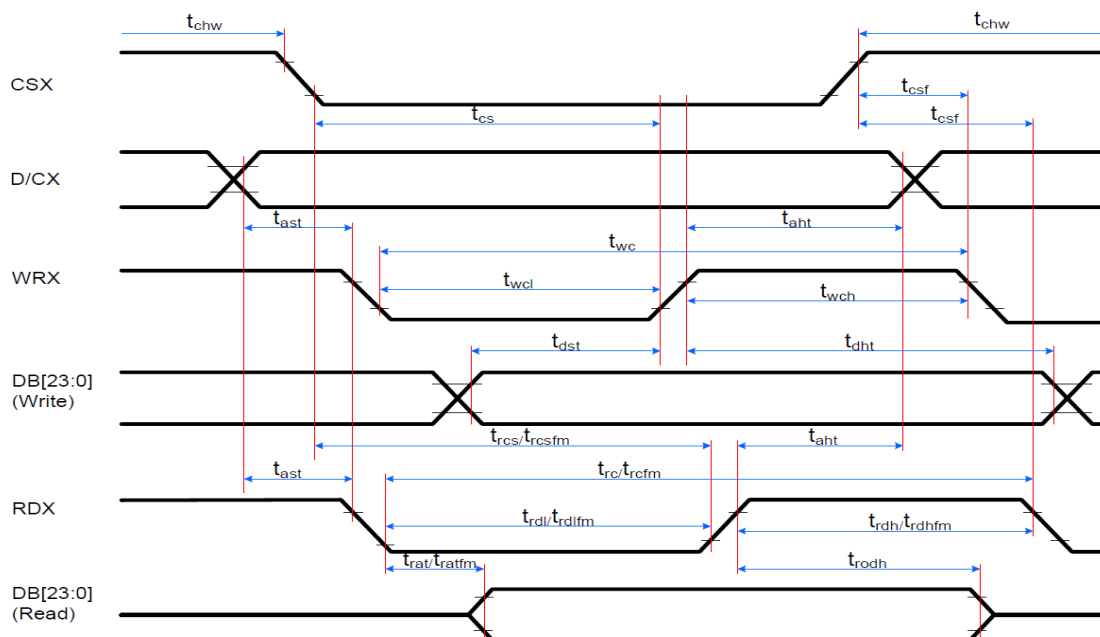


Figure 4.2.1 LED backlight circuit

4.3 Block Diagram



5 Timing Chart



Signal	Symbol	Parameter	min	max	Unit	Description
DCX	tast	Address setup time	0	-	ns	
	taht	Address hold time (Write/Read)	0	-	ns	
CSX	tchwh	CSX "H" pulse width	0	-	ns	
	tcs	Chip Select setup time (Write)	15	-	ns	
	trcs	Chip Select setup time (Read ID)	45	-	ns	
	trcsfm	Chip Select setup time (Read FM)	355	-	ns	
WRX	tcsf	Chip Select Wait time (Write/Read)	10	-	ns	
	twc	Write cycle	66	-	ns	
	twrh	Write Control pulse H duration	15	-	ns	
RDX (FM)	twrl	Write Control pulse L duration	15	-	ns	
	trcfm	Read Cycle (FM)	450	-	ns	
	trdhfm	Read Control H duration (FM)	90	-	ns	
RDX (ID)	trdlfm	Read Control L duration (FM)	355	-	ns	
	trc	Read cycle (ID)	160	-	ns	
	trdh	Read Control pulse H duration	90	-	ns	
D[17:0], D[15:0], D[8:0], D[7:0]	trdl	Read Control pulse L duration	45	-	ns	
	tdst	Write data setup time	10	-	ns	For maximum CL=30pF For minimum CL=8pF
	tdht	Write data hold time	10	-	ns	
	trat	Read access time	-	40	ns	
	tratfm	Read access time	-	340	ns	
	trod	Read output disable time	20	80	ns	

Note: $T_a = -30$ to 70°C , $V_{DDI}=1.65\text{V}$ to 3.3V , $V_{CI}=2.5\text{V}$ to 3.3V , $V_{SS}=0\text{V}$

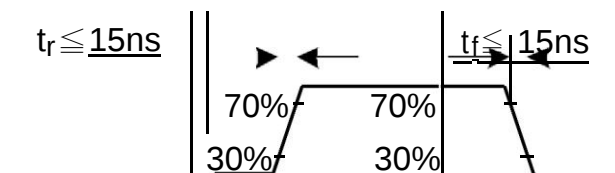


Table 5.1 timing parameter

5.2.Register write/read timing

a. Write to register

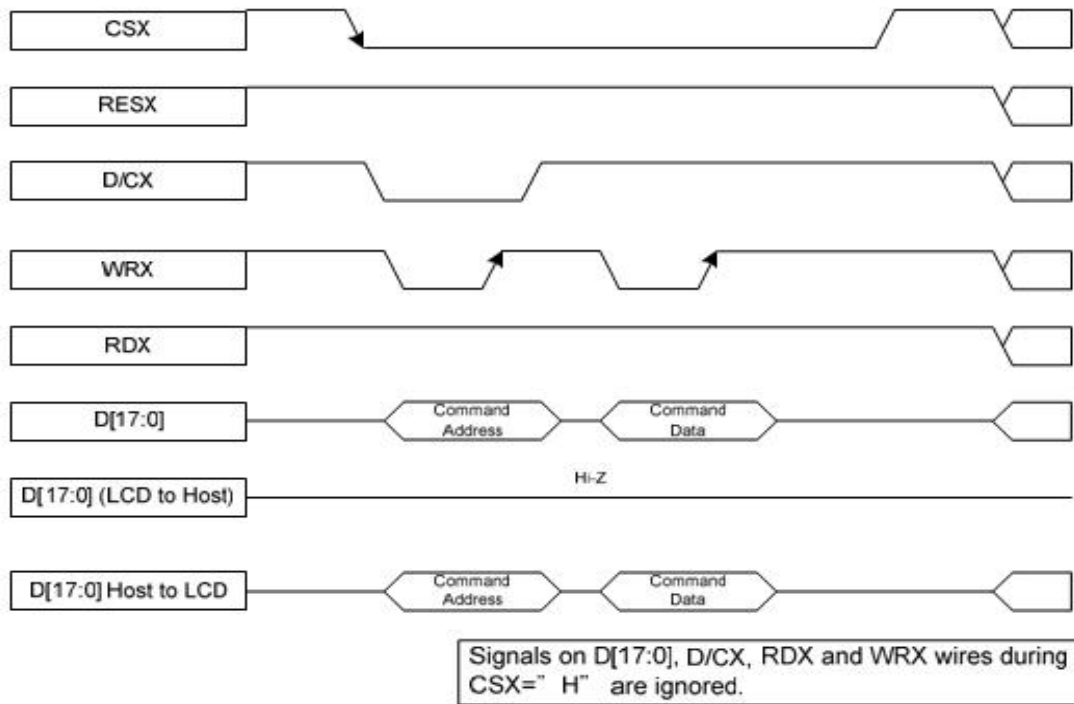
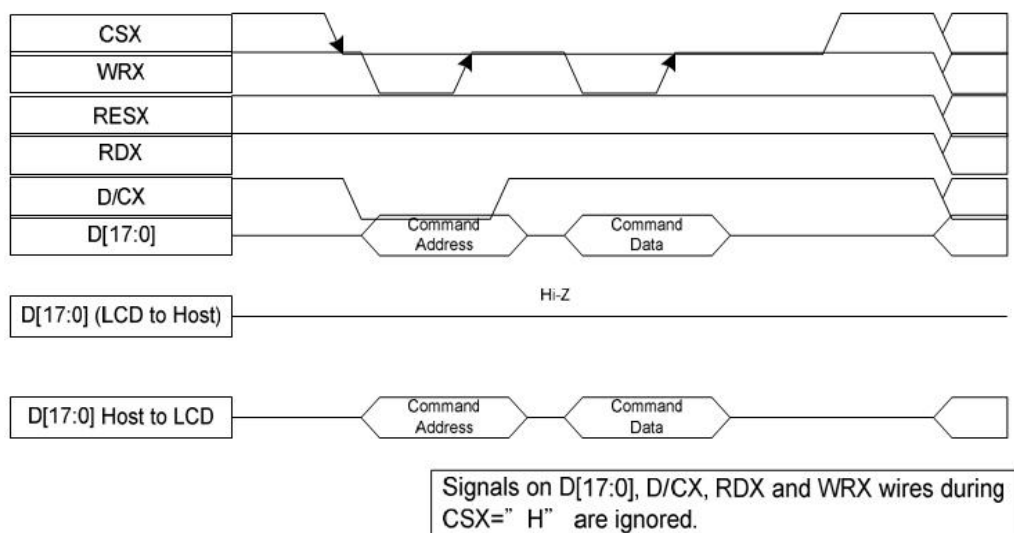


Figure 5.2.1 Register write timing in parallel bus system interface (for I80 series MPU)

b.Read from register



Note: Read data is only valid when the D/CX input is pulled high. If D/CX is driven low during read then the display information outputs will be High-Z.

Figure 5.2.2 Register read timing in parallel bus system interface (for I80 series MPU)

CS timings

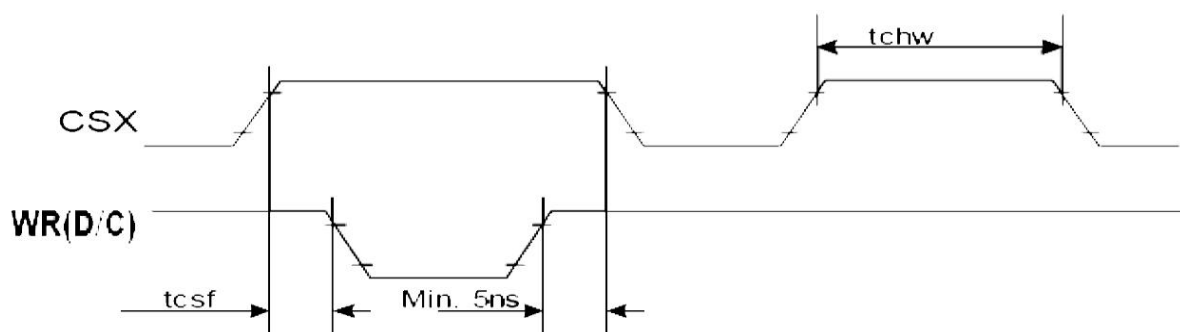


Figure 5.2.3 Chip selection timing

Write to read or read to write timings

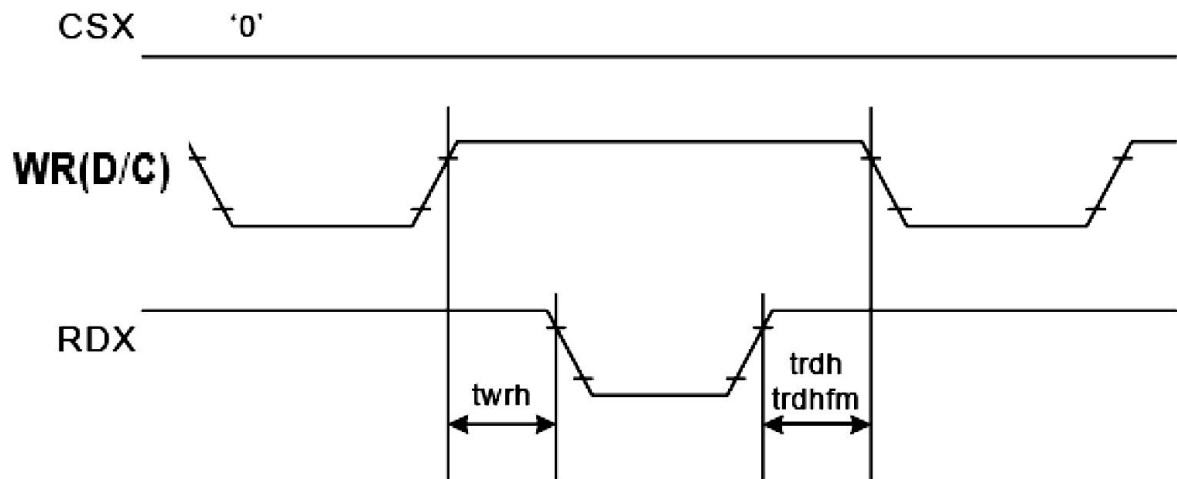
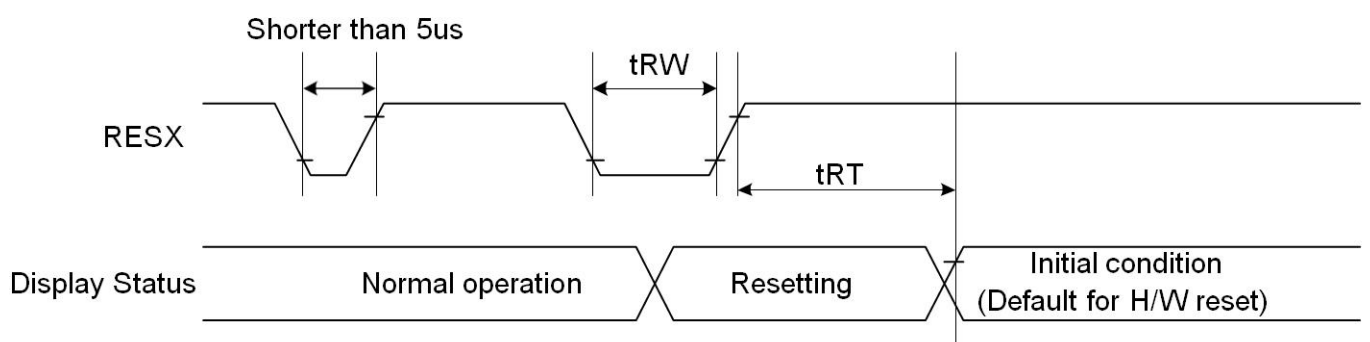


Figure 5.2.4 Write-to-read and read-to-write timing

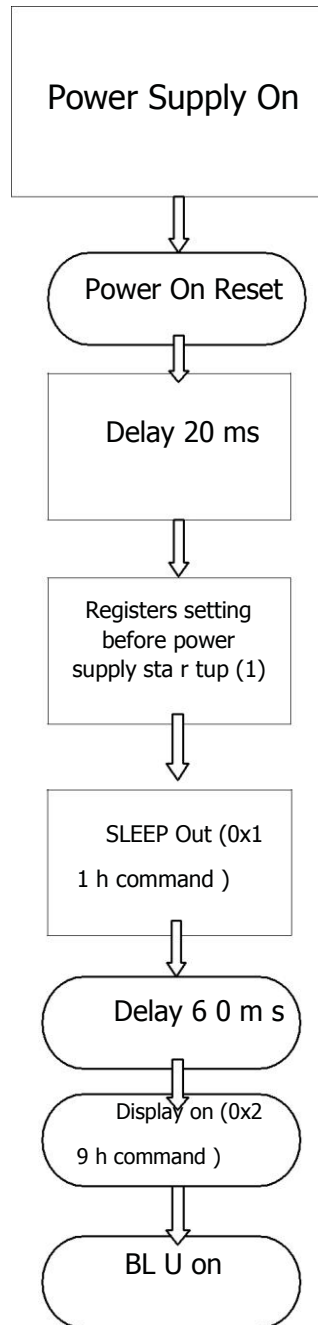
5. 3 Reset Timing Characteristics



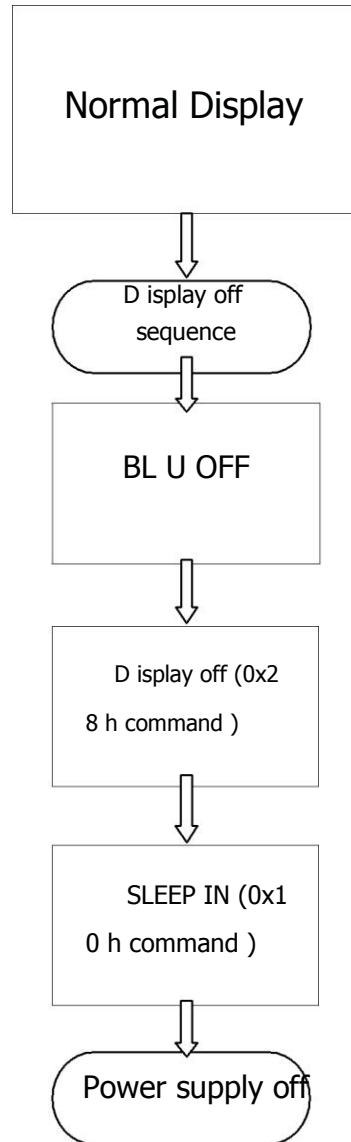
Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		uS
	tRT	Reset cancel		5 (note 1,5)	mS
				120 (note 1,6,7)	mS

Figure 5.3 RESET Timing

5.4 SLEEP OUT Sequence



5.4 SLEEP IN Sequence



6.Optical Characteristics

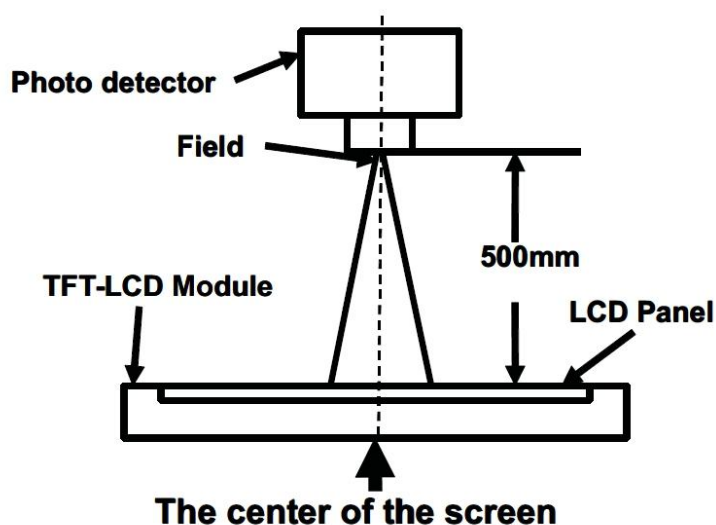
Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		0T	CRŠ10	70	70	70	Degree	Note2,3
		0B		70	70	70		
		0L		70	70	70		
		0R		70	70	70		
Contrast Ratio		CR	0=0 ⁰	400	500	400		Note 3
Response Time		T _{ON}	25℃		20	30	ms	Note 4
		T _{OFF}						
Chromaticity	White	x	Backlight is on	0.236	0.286	0.336		Note 1,5
		y		0.261	0.311	0.361		
	Red	x		0.530	0.580	0.630		Note 1,5
		y		0.270	0.320	0.370		
	Green	x		0.288	0.338	0.388		Note 1,5
		y		0.531	0.581	0.631		
	Blue	x		0.101	0.151	0.201		Note 1,5
		y		0.048	0.098	0.148		
Uniformity		U		-	80%		%	Note 6
NTSC				-	50%		%	Note 5
Luminance		L		180	300		cd/m ²	Note 7

Test Conditions:

1. I_F= 90mA, and the ambient temperature is 25℃.
2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

Note 3: Definition of contrast ratio

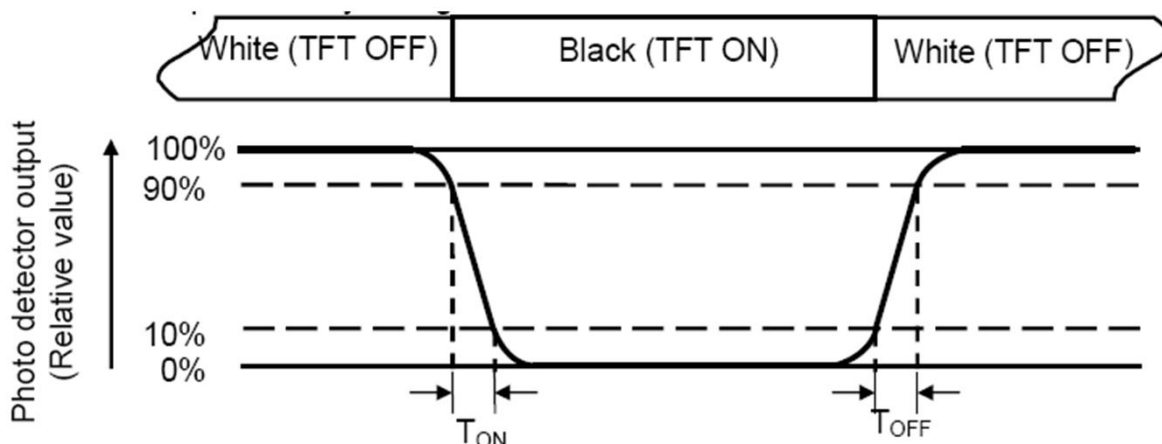
“White state “: The state is that the LCD should drive by Vwhite.

“Black state”: The state is that the LCD should drive by Vblack.

V_{white}: To be determined V_{black}: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

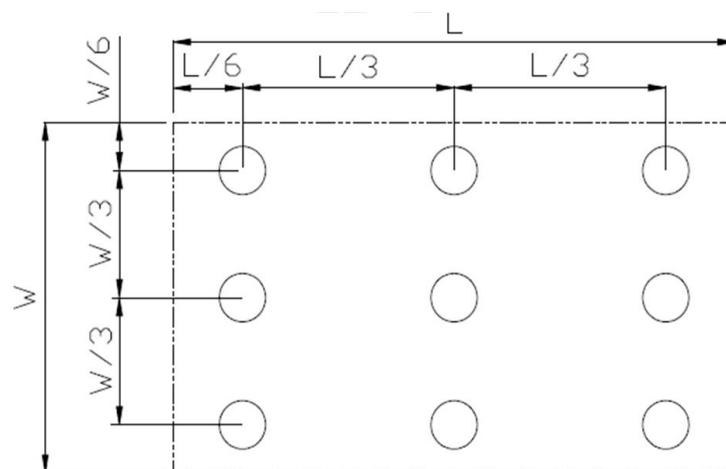
Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = L_{min} / L_{max}

Active area length W ----- Active area width



L_{max}: The measured Maximum luminance of all measurement position.

L_{min}: The measured Minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

7 Environmental I Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts=+70℃ 240hrs	IEC60068-2-1:2007 GB2423.2-2008
2	Low Temperature Operation	Ta=-20℃, 240hrs	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage	Ta=+80℃, 240hrs	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Storage	Ta=-30℃, 240hrs	IEC60068-2-1:2007 GB2423.1-2008
5	Storage at High Temperature and Humidity	Ta=+60℃, 90% RH 240 hours	IEC60068-2-78 :2001 GB/T2423.3—2006
6	Thermal Shock (non-operation)	-30℃ 30 min~+70℃ 30 min, Change time:5min, 20 Cycles	Start with cold temperature, End with high temperature, IEC60068-2-14:1984,G B2423.22-2002
7	ESD	C=150pF, R=330K>5points/panel Air:±8KV, 5times; Contact:±4KV, 5 times; (Environment: 15℃~35℃, 30%~60%, 86Kpa~106Kpa)	IEC61000-4-2:2001 GB/T17626.2-2006
8	Vibration Test	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total)(Package condition)	IEC60068-2-6:1982 GB/T2423.10—1995
9	Mechanical Shock (Non OP)	60G 6ms, ±X,±Y,±Z 3times, for each direction	IEC60068-2-27:1987 GB/T2423.5—1995
10	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32:1990 GB/T2423.8—1995

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

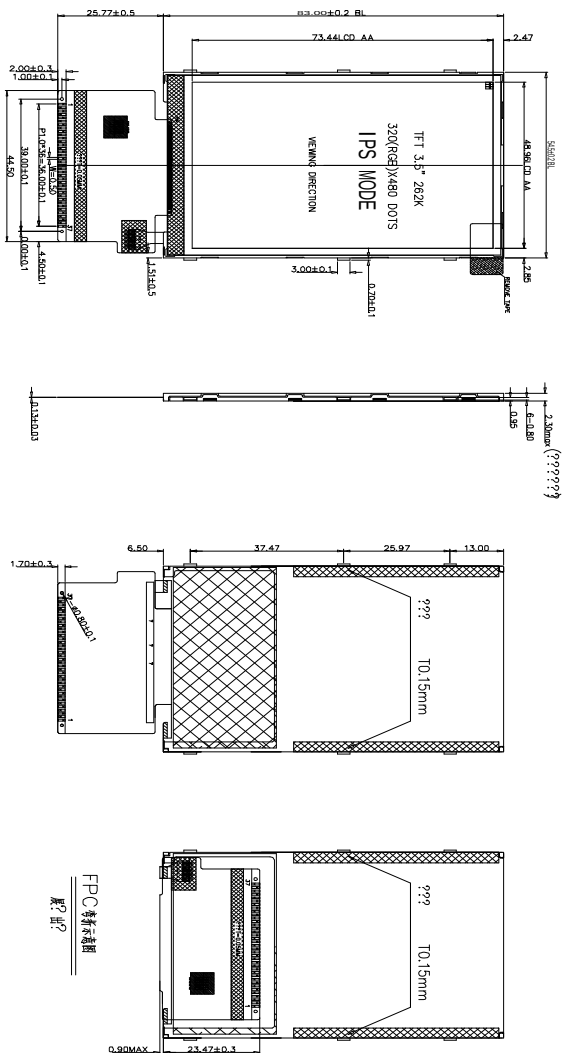
Note3: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Customer Approved/Data:

Rev.	AMENDMENT	DATE
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AMENDMENT	DATE
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Pad No.	Pad Name
1	GND
2	VDD
3	VDD
4	CS
5	RS
6	WR
7	RD
8	RESET
9	DPO
10	DB1
11	DB2
12	DB3
13	DB4
14	DB5
15	DB6
16	DB7
17	DB8
18	DB9
19	DB10
20	DB11

Technical drawing of a wall section showing a window and a door. The window has a width of 25.77 ± 0.5 and a height of 2.00 ± 0.1 . The door has a width of 1.00 ± 0.1 . The wall thickness is 250 mm. The drawing includes a section line and a detail callout 'D5TP'.

Figure 1 is a schematic diagram of the experimental setup. It shows a cross-section of a specimen with a central hole. The total width of the specimen is 100.0. The hole has a diameter of 50.0 and a depth of 10.0. A vertical line on the right is labeled 'SHEAR'.

Figure 1: Schematic diagram of the experimental setup. A subject is seated at a table, viewing a video screen. A camera is positioned above the screen. A target is placed on the table. A ruler is used to measure the distance from the subject's hand to the target. The distance is labeled as 10 cm.

PC 構造と配線

果？ 誰？

Diagram illustrating a 1D chain structure with 8 sites. The chain is represented by a vertical line on the left, labeled $A(\uparrow)$. Each site is represented by a circle containing a dot, with a downward arrow pointing to it from the vertical line. Below each site is a label: $K(-)$, $K(-)$, $K(-)$, $K(-)$, $K(-)$, $K(-)$, $K(-)$, and $K(-)$. The bottom site has an additional label $K(-)$ below it.

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9 Precautions for Use of LCD Modules

8.1 Handling Precautions

8.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

8.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

8.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

8.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

8.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

— Isopropyl alcohol

— Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

— Water

— Ketone

— Aromatic solvents

8.1.6 Do not attempt to disassemble the LCD Module.

8.1.7 If the logic circuit power is off, do not apply the input signals.

8.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

8.1.8.1 Be sure to ground the body when handling the LCD Modules.

8.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

8.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

8.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

8.2 Storage precautions

8.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

8.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0? ~ 40? Relatively humidity: “80%

8.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

8.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.